



TRW

PATENT

2664

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: William J. Goetzinger et al. : Date: March 10, 2006
Group Art Unit: 2664 : IBM Corporation
Examiner: Binh Quoc Nguyen : Intellectual Property Law
Serial No.: 10/015,994 : Dept. 917, Bldg. 006-1
Filed: November 1, 2001 : 3605 Highway 52 North
Title: WEIGHTED FAIR QUEUE SERVING : Rochester, MN 55901
PLURAL OUTPUT PORTS

03/14/2006 CNEGAI 00000011 090465 10015994
01 FC:1806 180.00 DA

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Mail Stop Amendment
Commissioner for Patents
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Alexandria, VA 22313-1450

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Serial No. 10/015,994
Docket No. ROC920010200US1

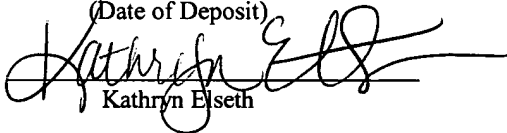
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Date: March 10, 2006

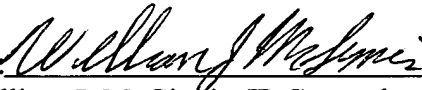
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March 10, 2006

(Date of Deposit)


Kathryn Elseth

Respectfully submitted,

By: 
William J. McGinnis, IP Counsel
Attorney Reg. No.: 25,698
IBM Corporation, Dept 917
3605 Highway 52 North
Rochester, MN 55901-7829
Telephone: (507) 253-5331



Approved for use through 07/31/2006. OMB 0651-0031

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Complete if Known

Application Number	10/015,994
Filing Date	November 1, 2001
First Named Inventor	William J. Goetzinger
Art Unit	2664
Examiner Name	Binh Quoc Nguyen
Attorney Docket Number	ROC920010200US1

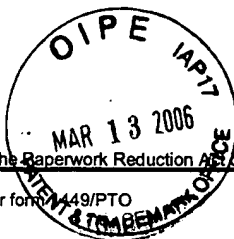
[illegible][illegible]Date
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

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First Named Inventor	William J. Goetzinger
Art Unit	2664
Examiner Name	Binh Quoc Nguyen
Attorney Docket Number	ROC920010200US1

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LYONS et al., "Estimating Clock Speeds for the ATMSWITCH Architecture", Proc. NETWORKS '99 (The Third New Zealand ATM and Broadband Workshop), 21-22, Jan. 1999, pps. 39-53.	

Examiner Signature	Date Considered
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